

are becoming more and more sensitive to process, voltage and temperature  
process variations are attributed to the imperfections in the manufacturing process.

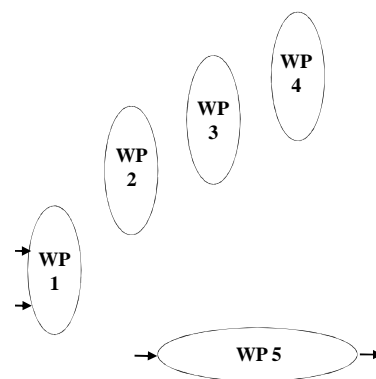
significantly affect the performance and behavior of circuits. Therefore it is  
it to deal with different effects of these variations.

**Reliability** : The art and the science of building computing systems that continue to work  
in the presence of faults due to PVT variations.

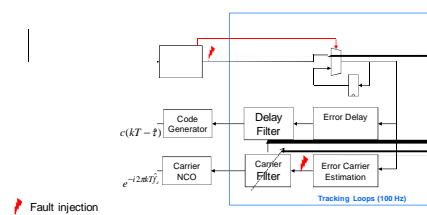
consumption of embedded systems

implementation of fault tolerant computation by studying the case of an existing application (a

analysis of the application on an ASIC (using a 28 nm CMOS technology) : a standard  
hardened GPS receiver tolerant to faults due to low voltage supply.



attempts to allow the tracking module of the GPS receiver to be more tolerant to  
voltage variations and the carrier discriminator.



- Modelling of Random Telegraph Noise inside a circuit.

$V_{in} = V_{SUPPLY} = 1 \text{ Volt}$

$V_{in} = V_{SUPPLY} = 0.5 \text{ Volt}$